

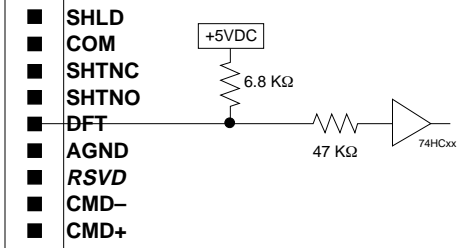
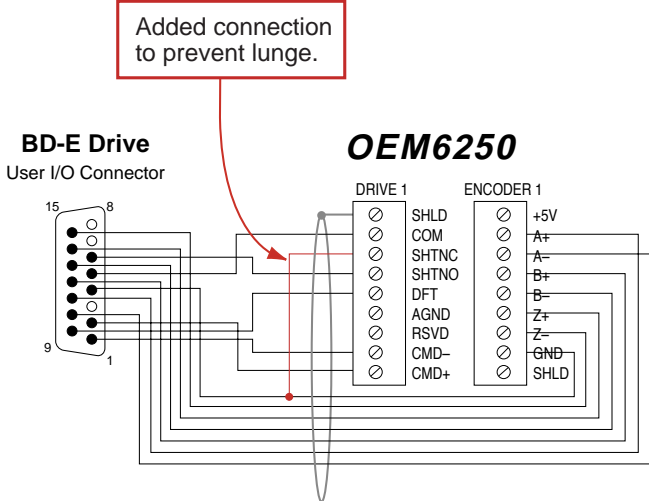
Change Summary

OEM6250 Installation Guide

Rev B

March 1998

The following is a summary of the primary technical changes to this document.
This book, p/n 88-016524-01B, supersedes 88-016524-01A.

Topic	Description																										
<p>Error Correction: DFT Input Circuit</p>	<p>Revision A incorrectly stated that the drive fault input (DFT pin on the DRIVE connectors) shared the same circuit design as the limit inputs and trigger inputs. DFT <u>is not</u> controlled by the AUX-P pullup terminal and <u>is not</u> affected by the R45 resistor. The DFT input circuit is:</p>  <ul style="list-style-type: none"> ■ SHLD ■ COM ■ SHTNC ■ SHTNO ■ DFT ■ AGND ■ RSVD ■ CMD- ■ CMD+ 																										
<p>Clarification: BD-E Drive Connections</p>	<p>With a BD-E drive connected as illustrated in revision A, the motor has a tendency to lunge for several revolutions at full torque when power is removed simultaneously from the OEM6250 and the BD-E drive (as would be the case in a power outage). The correction is to connect the OEM6250's SHTNC terminal to the BD-E's GND terminal (pin 4).</p>  <table border="1" data-bbox="495 1360 750 1711"> <thead> <tr> <th>BD-E Drive</th> <th>OEM6250</th> </tr> </thead> <tbody> <tr> <td>V2 (pin 1)</td> <td>↔ CMD-</td> </tr> <tr> <td>V1 (pin 2)</td> <td>↔ CMD+</td> </tr> <tr> <td>GND (pin 4)</td> <td>↔ GND</td> </tr> <tr> <td>RST (pin 5)</td> <td>↔ COM</td> </tr> <tr> <td>+15V (pin 6)</td> <td>↔ SHTNO</td> </tr> <tr> <td>FT (pin 9)</td> <td>↔ DFT</td> </tr> <tr> <td>AOP (pin 10)</td> <td>↔ A-</td> </tr> <tr> <td>AOP (pin 11)</td> <td>↔ A+</td> </tr> <tr> <td>BOP (pin 12)</td> <td>↔ B+</td> </tr> <tr> <td>BOP (pin 13)</td> <td>↔ B-</td> </tr> <tr> <td>ZOP (pin 14)</td> <td>↔ Z+</td> </tr> <tr> <td>ZOP (pin 15)</td> <td>↔ Z-</td> </tr> </tbody> </table> <p>NOTE: These connections will work only if BD-E jumper LK2 is set to position B (not the factory default position).</p>	BD-E Drive	OEM6250	V2 (pin 1)	↔ CMD-	V1 (pin 2)	↔ CMD+	GND (pin 4)	↔ GND	RST (pin 5)	↔ COM	+15V (pin 6)	↔ SHTNO	FT (pin 9)	↔ DFT	AOP (pin 10)	↔ A-	AOP (pin 11)	↔ A+	BOP (pin 12)	↔ B+	BOP (pin 13)	↔ B-	ZOP (pin 14)	↔ Z+	ZOP (pin 15)	↔ Z-
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